CFP: 4th Workshop on Power-aware Algorithms, Systems, and Architectures, PASA, 2015

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4th International Workshop on Power-aware Algorithms, Systems, and Architectures, PASA, 2015

http://pacman.cs.tsinghua.edu.cn/icpppasa2015/

Held in conjunction with

the 44th International Conference on Parallel Processing (ICPP-2015)

Beijing, China, September 1-4, 2015

http://icpp2015.tsinghua.edu.cn/

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Overview

Power-aware computing is becoming one of the top research priorities for a large variety of systems, including large-scale high performance computers and corporate data centers. We expect that more efforts will be invested in the power-aware computing, as a consequence of the continuous pushing towards higher performance and more compact system. Power-aware research requires both software and architecture innovation to improve power-efficiency while maintaining performance-oriented design. This workshop provides a forum to discuss the novel ideas about building power-aware software systems and architectures. It will present researchers the opportunity to develop new perspective on power-aware hot topics.

Areas of interest include but are not limited to the following topics:

- Power-aware runtime systems
- Power and performance modeling and evaluation
- Tools for power profiling, accounting and measurement
- System-level cross-layer coordination
- Hardware/software co-design to improve power efficiency
- Emerging power efficient storage device such as Solid-State Devices (SSD) and Storage Class Memories (SCM)
- Power-aware programming scheme
- Power-aware resource management strategies
- Novel architecting that assists in power management
- Thermal control and reduction

Important Dates

- Paper Submission Deadline: May 23, 2015, 11:59 PM, AOE, Hard Deadline
- Paper Acceptance Notification: June 5, 2015
- Camera Ready Due: June 15, 2015
- Workshop: September 1, 2015

Submission Guidelines

Submitted manuscripts should be formatted according to ICPP 2015 proceedings guidelines:

- The CPS standard, double-column format.
- A font size 10pt or lager.
- The maximum length is 10 pages (all inclusive).
- Submissions should represent original, substantive research results.
- Templates can be found [here](http://www.ieee.org/conferences_events/conferences/publishing/templates.html) (http://www.ieee.org/conferences_events/conferences/publishing/templates.html)
- Submissions should be made online at https://easychair.org/conferences/?conf=icpppasa2015

The published papers will be available at the conference in CD format, and will be submitted to IEEE Xplore.

Organizers

Steering Committee

- Dimitrios S. Nikolopoulos, Queen's University of Belfast, UK.
- Dong Li, Oak Ridge National Laboratory, USA.

Program Chairs

- Jidong Zhai, Tsinghua University, China.
- Guangming Tan, Chinese Academy of Sciences, China.

Program Committee

- Hanhua Chen, Cluster and Grid Computing Lab, Huazhong University of Science and Technology, China.
- Bingsheng He, Xtra Computing Group, Nanyang technological University, Singapore.
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- Kazutomo Yoshii, Mathematics and Computer Science Division of Argonne National Laboratory, USA.
- Hucheng Zhou, System Research Group, Microsoft Research Aisa, China.
- Peng Zhang, Biofluid Laboratory, Stony Brook University, USA.

Web Chair

Zhen Zheng, Tsinghua University, China.

Contact

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