

Feng Zhang

CONTACT INFORMATION

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RESEARCH INTERESTS

Big Data, Parallel Computing, and Architecture Optimization

EDUCATION

Tsinghua University, Beijing, China

Ph.D., Computer Science and Technology, *Expected: Summer 2017*

- Topic: *Analysis and Optimizations toward Integrated Architectures*
- Advisors: Wenguang Chen, Ph.D and Jidong Zhai, Ph.D

Xidian University, Xi'an, China

B.S., School of Computer Science and Engineering, July 2012

- *Summa Cum Laude*

RESEARCH EXPERIENCE

Visiting Scholar

April 2016 to November 2016

Department of Computer Science,
North Carolina State University, Raleigh, NC, USA
Supervisor: Xipeng Shen, Ph.D

Research Assistant

September 2012 to present

Department of Computer Science and Technology,
Tsinghua University, Beijing, China
Supervisors: Wenguang Chen, Ph.D, Jidong Zhai, Ph.D

Intern

August 2013 to December 2013

Deep Neural Networks Research Group
AMD, Beijing China
Mentor: Junli Gu, Ph.D

CONFERENCE PAPERS

- 1 [CGO'17] **Feng Zhang**, Bo Wu, Jidong Zhai, Bingsheng He and Wenguang Chen, FinePar: Irregularity-Aware Fine-Grained Workload Partitioning on Integrated Architectures. *Proceedings of the 2017 IEEE/ACM International Symposium on Code Generation and Optimization (CGO)*, February 4-8, 2017, Austin, Texas, USA.
- 2 [MASCOTS'15] **Feng Zhang**, Jidong Zhai, Wenguang Chen, Bingsheng He and Shuhao Zhang, To Co-Run, or Not To Co-Run: A Performance Study on Integrated Architectures. *IEEE 23rd International Symposium on Modeling, Analysis and Simulation of Computer and Telecommunication Systems (MASCOTS'15)*, October 5-7, 2015, Atlanta, GA, USA.
- 3 [ICPPW'15] **Feng Zhang**, and Liang Chen, HPC-Oriented Power Evaluation Method. *IEEE 44th International Conference on Parallel Processing Workshops (ICPPW)*, September 1-4, 2015, Beijing, China.
- 4 [APSys'14] Junli Gu, Maohua Zhu, Zhitao Zhou, **Feng Zhang**, Zhen Lin, Qianfeng Zhang, Mauricio Breternitz, Implementation and Evaluation of Deep Neural Networks (DNN) on Mainstream Heterogeneous Systems. *Proceedings of 5th Asia-Pacific Workshop on Systems (APSys14)*, ACM, June 25-26, 2014, Beijing, China.

- JOURNAL PAPERS
- 1 [TPDS'16] **Feng Zhang**, Jidong Zhai, Bingsheng He, Shuhao Zhang, Wenguang Chen, "Understanding Co-running Behaviors on Integrated CPU/GPU Architectures", *IEEE Transactions on Parallel and Distributed Systems (TPDS)*.
 - 2 [SCIS'15] Jidong Zhai, **Feng Zhang**, Qingwen Li, Wenguang Chen, Weimin Zheng, "Characterizing and optimizing TPC-C workloads on large-scale systems using SSD arrays", *SCIENCE CHINA Information Sciences*, 19 April 2015.
- PERSONAL PROFILE
- Specialties: accelerators, AMD GPU/CPU/APU, NVIDIA GPU, Intel GPU/MIC
 - Strong programming skills in OpenCL
 - Familiar with parallel programming language (CUDA, OpenMP, MPI, Pthread)
 - Document Analytics using Spark on Amazon EC2
 - Language: English and Chinese
- AWARDS
- National Scholarship, Tsinghua University, Beijing, China Oct 2016
 - Guanghua Scholarship of Tsinghua University, Beijing, China Oct 2015
 - Guanghua Scholarship of Tsinghua University, Beijing, China Oct 2014
 - Guanghua Scholarship of Tsinghua University, Beijing, China Oct 2013
 - Outstanding Graduates of Xidian University, Xi'an, China July 2012
 - National Scholarship, Xidian University, Xi'an, China Oct 2011
 - National Scholarship, Xidian University, Xi'an, China Oct 2010